

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Jens Barrenscheen et al.

Application No.: 10/727,102

Confirmation No.: 4397

Filed: December 2, 2003

Art Unit: 2181

For: Arrangement comprising a first
semiconductor chip and a second
semiconductor chip connected thereto

Examiner: C. K. Lee

SUBMISSION WITH REQUEST FOR CONTINUED EXAMINATION (RCE)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated April 24, 2006, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.